NOTICE WARNING CONCERNING COPYRIGHT RESTRICTIONS:

The copyright law of the United States (title 17, U.S. Code) governs the making of photocopies or other reproductions of copyrighted material. Any copying of this document without permission of its author may be prohibited by law.

TRANSMISSION FACILITY PLANNING IN TELECOMMUNICATION NETWORKS: A HEURISTIC APPROACH

by

^{*}I. Baybars & K.O. Kortanek

December, 1982

DRC-21-13-82

Transmission Facility Planning in Telecommunication Networks: A Heuristic Approach

by

I. Baybars⁽¹⁾ and K.O. Kortanek⁽²⁾

October 18, 1982

(1) $_{\rm Graduate \ School \ of \ Industrial \ Administration}$.

(2)

Department of Mathematics

The research of both authors is partially funded by National Science Foundation Grant ECS-8026777 and ECS-8209951.

> UNIVERSITY LIBRARIES CARNEGIE-MELLON UNIVERSITY PITTSBURGH, PENNSYLVANIA 15213

ABSTRACT

The transmission system cost functions can be decomposed - approximately - into a fixed charge part and linear cost part. These parts represent the initial investment cost of installing a transmission system on a specific link of the network and the cost of installing circuits of that system, respectively. We present a mixed integer programming (MI?) model to minimize the present value of facility installation costs subject to satisfying linkwise circuit requirements in each period of a fixed planning horizon* The model treats (i) alternate transmission systems with limited supplied, (ii) general circuit requirements and (ill) pre-specified alternate routes, for circuit assignment.

A heuristic procedure is developed for obtaining approximate optimal solutions for a case of empirical interest, where transmission supplies are unlimited <u>main</u> where there is only one alternate route for circuit assignment. Numerical results are presented for moderate size facilities networks over a 3 period planning horizon.

.

1. INTRODUCTION

1.1 <u>Telecommunications_Networks</u>

A <u>telecomminications network</u> is a collection of <u>junctions</u> (or <u>points</u>) some or all of which are joined by direct communication links. A <u>link</u> is a collection of facilities known as <u>transmission equipment</u> which when taken * together comprise a <u>transmission system</u> such as cables, radios, satellites, etc. The main components of a transmission system are the <u>circuits</u>, such as wires, frequencies, channels, etc. One of the traditional transmission facilities is the <u>cable</u> consisting of a large number of <u>wires</u>. Recently, however, the use of radios, satellites, and fiber optics has been rather common.

Traffic, in the form of voice telephone calls, <u>originate</u> at a junction A, such as a city, to be transmitted to another junction B, termed the <u>destina-</u><u>tion</u>. If there is a direct link in the network which joins point A to point B, the call is transmitted through that link as long as not all the circuits of that link are in use at a given time. If all the lines of the direct A-to-B link are busy, then there are two possibilities: the caller may receive a message requesting that the destination number be re-dialed at a later time, or if there are switching facilities at A, the A-to-B traffic may be switched to some other link, say A-to-C. The call can then be transmitted to B through the links A-to-C and C-to-B or via some other sequence of direct links. In this paper switching facilities shall be distinct from transmission facilities, and emphasis will be on planning models for the latter only.

A telecommunication network can be pictorially represented by a graph whose "vertices¹¹ and "edges¹¹ correspond to the "points¹¹ and the "direct communication links" of the network, respectively. The graph of <u>Figure 1</u> represents a telecommunications network with 8 points and 15 direct links.

From hereon, "link" shall mean "direct communication link¹¹. Furthermore, we will not distinguish between A-to-B and B-to-A traffic. Our graph theoretic terminology is standard; for definitions not given here, see Harary [9]. A link joining point i to point j will be denoted by the doublet (i, j). Oc-casionally, a link will be represented by e., when the point-pair it connects \int_{J}^{J} need not be distinguished.

1.2 Transmission Facility Planning in Telecommunications Networks

The process of facilities planning consists of two major steps. Originating demand for a service such as voice transmission is estimated in units of traffic load, typically erlangs, or hundred call seconds (CCS) per hour, where 36 CCS equals one erlang. Actually, to assess the eventual alterations of the network one requires estimated traffic for the peak times of the year ("busy-season, busy-hour"). It is necessary to first translate these demands into transmission channels or trunks which by definition are dimensionless units with a single trunk being needed to carry on a two-way voice communiuation. One could term this first step of the planning process as <u>trunking analysis</u>, and an example of an optimization approach to this task is given in Kortanek, Lee, and Polak [11]. The approach in [11], as well as many others, employs a network hierarchy which permits blocked traffic on a link to be switched through other junctions, eventually reaching the intended destination. The idea of alternate routing appears to have originated in a classic paper of Truitt [17] in 1954.

The output of the trunking analysis is a list of trunks between all point pairs (including 0 trunks between some point pairs). Normally, these requirements for trunks would be computed in the short run, for a given year, say t_o. The trunk requirements get satisfied by actual facilities installation re*. gardless of their level of technology (e.g., overhead cable vs. underground fiber optics). But facilities alterations are definitely long run phenomena, since, once in place, such facilities remain so for possibly 20 to 30 years comprising the <u>planning horizon</u>. Therefore, it is necessary to obtain trunking requirements for the long run also, say for years t_o, t_o + 1,...,t_o4- 29. One way of doing this is to first estimate customer calling demand in CCS

between point pairs for each year of the planning horizon. Then, repeated implementation of a trunking analysis procedure such as the one given in [11] would yield point-to-point trunking requirements for each year of the planning horizon.

At this point of the overall facilities planning process, it is convenient to anoint the so-computed trunks as inputs to a facilities planning model by referring to them formally as <u>circuits</u>. The task then at this stage may be termed the <u>transmission facilities planning problem in telecommuni-</u> <u>cations networks</u>: given point-pair circuit requirements for each year in the planning horizon, find a minimum present value cost facility installation plan by specifying the type of transmission systems and the links themselves on which the systems are to be installed as well as the number of circuits to be installed on each such link in each period of the finite planning horizon. Formally, this combinatoric optimisation problem is a fixed-charge multi-commodity flow synthesis problem and is an enormously difficult one to solve (see, for instance, Lawler [12]. A recent survey by Luss [13] on capacity expansion problems provides an excellent discussion and comparison of similar problems and solution approaches presented in [5], [7], [16], [18] and [19]. Other relevant works include [1], [2] and [3].

Our purpose here is to attack this computationally intractable problem through approximate, tractable means by simplifying the problem as described below.

Basically, an overall model formulation such as Yaged [18] permits imputed circuit demand to be literally "routed¹¹ along any sequence of links joining any particular pair of points. Concurrent with this task is the determination of actual facility equipment for any or all of these links. The idea of our formulation is to severely limit the number of choices for routing che circuit requirements of pre-specified links. For example, the simplest

model approximation would permit any circuits required for a point-pair (a) to be installed on that direct link or (b) routed along the uniquely determined alternate route stemming from the original, a priori, network hierarchy underlying the trunking analysis which generated the circuit inputs for the planning horizon in the first place* This approximation is designed to be consistent with the given network hierarchy. The model that we present in <u>Section 3</u> allows more complicated hierarchies in the sense that some point pairs may have many alternate routes.

2. A GRAPH THEORETIC-MATHEMATICAL PROGRAMMING APPROACH

2.1 <u>Network Hierarchy</u>

In most telecommunication networks not all pairs of points are connected by links because of obvious economic reasons. In graph theoretic terms, this means that the corresponding graph is non-complete. However, we assume that the graph is connected and, therefore, given switching facilities, it is possible to reach any point of the network from any other point.

Consider a network represented by the graph of <u>Figure 2</u>. Suppose that the dashed edges of this graph correspond to links which can only carry its own traffic. For instance, the link (3,5) can only carry point 3-to-point 5 traffic. Note that there is no link between points 4 and 6. This means that the point 4-to-point 6 traffic is transmitted along links (4,7) and (7,6). If all the line-circuits of the link (4,7) or (7,6) are busy, then the 4-to-6 traffic is blocked, i.e. lost.

On the other hand, there is a link, namely (5,7), joining point 5 to point 7, and, therefore, the point 5-to-point 7 traffic can be carried along this link. However, if all the circuits of link (5,7) are busy, then the excess 5-to-7 demand can be transmitted along links (5,1), (1,2) and (2,7) subject to idle capacity. The designation of a subset of the links of the telecommunication network in this fashion results in a simple hierarchy: a link (A,B) is termed <u>high-usage</u> if (i) (A,B) can not carry the traffic of pairs of points other than that of A and B, and (ii) it is possible to transmit the A-to-B traffic via an alternate route. All other links are termed <u>final</u>. Thus, by definition, the excess traffic on a final link is lest; the (excess) traffic on a high-usage link can be switched to an alternate route.

This particular type of network hierarchy can be described in graph theoretic terms as follows: the final links are chosen in such a manner that the graph induced by the corresponding edges is a spanning tree, i.e. a connected graph with no cycles. As shown in Figure 3, the solid edges of the graph of <u>Figure 2</u> induce the spanning tree T. (A graph G_S is a <u>spanning</u> <u>subgraph</u> of the graph G if G_S contains all the vertices of G and no other vertex.) The remaining links of the network are the high-usage links, and they are, in graph theoretic terms, the chords of that spanning tree. Thus, T, together with any chord, contains exactly one cycle. For instance, the graph G_1 of <u>Figure 4</u> is T together with its chord e_{15} and it contains exactly one cycle: $e_{15}e_2e_1e_3e_7e_{15}$. This implies that there is one and only one path in G_1 joining the end vertices of the edge e_{15} , namely $e_2e_1e_3e_7$. A similar case actually is true for any chord (high-usage link) adjoined to T. Thus, designation of the network hierarchy in this manner offers a unique alternate path for the traffic of each high-usage link. By the length of an alternate path we shall mean the number of final links which it contains.

As an approximation to all conceivable routings, we describe this simplest of all network hierarchies as follows. The circuit requirements of a high-usage link can be met in two ways: either by installing a system -and therefore, circuits -- on that link or by wholly or partially meeting the demand through routing along the links of its unique alternate route. Even under this restricted routing plan it follows that more than one set of circuits can be installed to meet the requirements of a particular high-usage link. While the fixed cost of installing a system is relatively large, the fixed cost per unit of capacity, as well as the variable cost of

circuit installations decreases as system capacity increases. As a consequence, economies of scale may be realized by installing larger systems for which it may be more economical to route the circuit requirements of (some) of the high-usage links than install transmission equipment on a high-usage link itself. This will become apparent when we present numerical examples in <u>Section 3.3</u>.

2.2 Assumptions

A network topology and-network hierarchy of Section 2.1 is given and fixed. There is no provision for the installation of switching equipment or multiplex equipment at the nodes; nor for the installation of new links in the hierarchy. In addition, all input parameters such as network costs, circuit requirements, and system capacities are assumed to be known constants. No monotonicity assumptions, however, are placed on circuit requirements over the planning horizon.

We shall assume that the life of each transmission facility exceeds the length of the given planning horizon and that end-of-planning horizon effects are negligible.

la. the telecommunications field it has been assumed that cost functions associated with installing transmission systems are <u>concave</u>, reflecting economies of scale. These functions may be decomposed – approximately – into a fixed charge and a linear cost part. The fixed charge part represents the initial investment cost of installing a transmission system (e.g., cable) on a link. The linear cost part, on the other hand, represents the cost of installing the circuits (e.g., wires in cables) of that system. Generally, it is assumed that both of these costs depend on the length of the individual links (i.e., the actual distance between the two points joined by that link).

We assume that there are <u>alternative transmission systems</u> such as cables, satellites, microwave radios, and that any of these systems may be available for only specific periods of the planning horizon. Their supply is limited, as well as the supply of the individual circuits of the specific systems.

We shall assume that system reliability will be enhanced if more than one type of transmission facility is present on each link. This would enable the users to maintain direct contact between specific pairs of points, if, for instance, the links are installed with cables as well as satellites facilities, in case the satellite may fail.

3 <u>A GENERAL MIXED-INTEGER PROGRAMMING >K)DEL</u>

We shall use the following notation:

L * {links}; I will denote a link and $q^{s} | L$

- H » {high-usage links}; h will denote a high-usage link and $q^1 \le |H|$
- F » {final links}; f will denote a final link and q" a |FI
- Ti(h)^{*} {final links which constitute the i(h)th alternate route for high-usage link h_f i(h) €lh - {l(h), 2(h),..., h(h)}
- H_{f} « {high-usage links for which at least one alternate route contains the final link f}
- cj : cost of installing one system s unit on link *I* in period t, where t€T * fl,...,t^f} and s€S * {l,...,s'}

c? : cost of installing one system s circuit on link K in period t
b : number of system s units available in period t

- B : number of system s units available throughout the planning horizon
- a : circuit capacity of one system s unit

a . : number of system s circuits available for installation in sc

period t

A^{\$} .: number of system s circuits available for installation throughout the planning horizon

r : annual interest rate

w^c « (1+r)" : discount factor for t years

k^s : parity factor

 $d:^{t}$: circuit requirement of link L in period t

The variables under control are as follows:

^xlat : number of system s units installed on link I in period t

YLat: number of system s circuits Installed on link I in period t

.^Ui(h), t: ^{the} number of circuits which will be routed to the i(h) th

alternate route of the high-usage link h in period t.

Before presenting the model, we first note that H U F • L and H D F • 0. For the network depicted in <u>Figure 2</u>, $\overline{h}(h) \cdot 1$ Y h c H and;

^F1(8) * t^{1} , ²}' ^F1(9) * fr.3.5}.^F1(10) -.Cl.3.6}. F_{1(U)} - (2,1,3)

 $F_{1(12)} = \{2,1,4\}, F_{1(13)} = \{2,1,3,5\}, F_{1(M)} = \{2,1,3,6\}, F_{1(15)} = \{2,1,3,7\}.$ On the other hand, $\overline{h}(h) = 2 V h C H$ in the network of <u>Figure 5</u>:

 $F_{1}(9) = J_{1}^{2} F_{2}(9) = t^{3} F_{1}(10) = C^{1} F_{2}(10) = \{4, 5, 6\}$ $F_{1}(11) = \{4, 3, 8\}, F_{2}(11) = \{5, 6, 1, 2, 8\}, F_{1}(12) = \{3, 4\}, F_{2}(12) = \{2, 1, 6, 5\}$ $F_{1}(13) = \{2, 7\}, F_{2}(13) = C^{1} F_{1}(13) = (2, 7), F_{2}(13) = C^{1} F_{1}(13) = C^{1} F_{2}(13) = C^{1} F_{1}(13) = C^{1} F_{2}(13) = C^{1} F_{1}(13) = C^{1} F_{1}(13) = C^{1} F_{1}(13) = C^{1} F_{1}(13) = C^{1} F_{2}(13) = C^{1} F_{1}(13) = C^{1} F_{2}(13) = C^{1} F_{1}(13) = C^{1} F_{1}(13) = C^{1} F_{1}(13) = C^{1} F_{2}(13) = C^{1} F_{1}(13) = C^{1} F$

And, finally, $\overline{h}(h) - 3$, V h « H in the network of Figure 6 ;

F1(10) =
2
, 4 , 5 , 2 (10) = 1 , 9 , 6 , 5 , 3 (10) = 3 , 7 , 3
F1(U) = 4 , 5 , 6 , 5 , 2 (U) = {2,3,7,5,6}, 5 , 5 , 3 (u) - {2,1,9}
F1(12) = {5,6,9}, {}^{5}, 2 (12) = 4 , 2,1,9}, 5 , 3 (12) - {7,3,1,9}
F1(13) = {5,6,9}, {}^{1}, 5 2(12) = 4 , 2,1,9}, 5 , 3 (12) - {7,3,1,9}
F1(13) = {5,6,9}, {}^{1}, 5 2(13) = CL2.4J, 5 , 3 (13) - {1,3,7}
F1(14) = {1,2,4,8}, {}^{5}, 5 , 2 (14) - {9,6,5,8}, {}^{5}, 5 , 3 (u) - {1,3,7,8}
F1(15) = {6, {}^{5,7}!, 5 2(15) = {3,1,9}

We can now present the model:

<u>Program P</u>

minimize

$$z - Z Z Z w (cJ x + c'' y)$$
(1)

$$i \in L \text{ scS teT}^{c} < s + c'' + c''' + c'' + c$$

from among X_{lst}, Y_{lst}, u_{i(h)t} for each XeL

•

subject Co:

t t

$$E \mathcal{J}_{ist} \leq e_{s} \sum_{t=1}^{s} \mathcal{I}_{st}$$
 $\forall l \in L, V \le S, V t = T$ (4)

 $\begin{array}{ccc} \mathbf{E} & \mathbf{Z} \mathbf{x}_{\boldsymbol{\ell} \mathbf{st}} \leq \mathbf{B}_{\underline{s}} & \text{VseS} & (5) \\ \\ UL & \text{teT} & \end{array}$

$\sum_{i=1}^{\infty} x_{ist} \leq b_{st}$	VseS, VteT	(6)

 $\sum_{LtL \text{ ceT}} \sum_{y_{lst}} \leq A_s \qquad \text{VseS} \qquad (7)$

∑ y _{lst} ≤ [±] st	VseS, VteT	(8)
--------------------------------------	------------	-----

$$\vec{t}_{1} \quad \vec{t}_{kit} \rightarrow \hat{t}_{0} \quad sjs \quad \vec{t}_{l} \quad v_{k} \in v_{k} \quad v_{l} \quad$$

Description of Program P:

The objective function (1) is the sum of the discounted fixed costs of installing transmission systems and the discounted variable costs of instal-

The first two constraints represent "circuit requirements¹¹: The number of circuits on a final link f in period \overline{t} must be greater than or equal to the sum of the circuits required for that link and the circuits to be used to (partially) meet the requirements of the high-usage links whose alternate

route(s) contain f. This requirement is expressed by (2). (3), on the other hand, is for high-usage links: the number of circuits on a high-usage link h in period t must meet the demand not satisfied by re-routing. (4) represents the capacity of system s units: system s circuits can be installed in link t in period t only if system s units have been installed in periods $I_92_9..._9t$ and there is idle capacity. (5) represents the limited supply of a specific transmission system over the planning horizon whereas (6) represents the limited supplies periodvise. Similarly, (7) represents the limited supply of the circuits of a specific transmission system over the planning horizon and (8) represents the limited supplies of the circuits in each period of the planning horizon. (9) is the parity constraint: in order to avoid dependency on a sole system on a link, at least a specified proportion k_{\bullet} of circuits on every link must be of system s type.

<u>Program ?</u> is a rather large program. Let q^{ffl} be the number of alternate routes In the network, i.e.,

Then, P has $qt^f (1+2s^f) + 2s^f (1+2t^f)$ constraints other than non-negativity requirements and $t^f (2qs^f 4q^{fff})$ variables, $qs^f t^f$ of which are integer. (We should note here that the number of circuits installed on a link in real-life is sufficiently large enough to enable us to permit fractional values for the variables y_{s1t}^{fs} -) For instance, the number of constraints would be 357 if $s^f \gg t^1 * 3$ and $q^f \gg 7$, $q^m * 8$ and $q^{lff} * 8$; the number of variables would be 294 with 135 integer variables. Thus, even for relatively small s^f and t^f , <u>Program P</u> remains large.

15.

A reduced problem, which has empirical interest for planning, may be obtained by deleting the supply of equipment constraints (5)-(8-> and the parity constraints (9), and by restricting the number of alternate routes of each high-usage links to be one.

In addition we make a simplifying assumption on costs, namely all costs are independent of link length. In other words unit circuit costs are independent of the subscript f (corresponding to link 4). This assumption is plausible for networks having links approximately the same length, or at least where the fixed costs are somewhat insensitive to link length.

We shall denote the resulting simplified mixed-integer program as <u>Program</u> &, which in terms of \pounds . consists of (1)-(4), (10), (11) with Ih = {l(h) } for each heH.

Our previous computational experiences in solving <u>Program Q</u> for illustrative problems are reported in [3]. <u>Program Q</u> is sufficiently large and complex that in our attempts to solve it using "flow theoretic methods" [3] and "general purpose mixed 0-1 programming codes" [14] numerical difficulties were encountered. The magnitude of this computation time led us to seek a methodology capable of finding a "good" solution, rather than the optimum, in a "reasonable" CPU time, two terms which are difficult to define in this context. The need for heuristics for problems of this type are also made clear by Luss [13].

4. A HEURISTIC METHOD FOR SOLVING PROGRAM Q

In order to be able to find a "good" solution in "reasonable" CPU time, we adopted a number of simple rules and constructed a seven step heuristic procedure called HTCPQ (See Appendix). The procedure assumes that |Ih| = 1, $\forall h \in H$, i.e. we consider only <u>one</u> alternate route for each high-usage link. Hence, F_h will represent $F_{1(h)}$. We shall now present a summary of HTCPQ.

Given the network configuration, we first determine the sets F_h and H_f , and all control variables are set equal to zero. At this point, if the circuit requirements of a final link \overline{f} in period 1 exceeds the circuit capacity of the largest transmission system, a decision is made to install as many units of that system as necessary so that the updated circuit requirement of \overline{f} prior to the implementation of <u>Step 1</u> is less than the circuit capacity of the largest system.

In <u>Step 1</u> we determine the collection of high-usage links whose circuit requirements should <u>not</u> be routed. This decision is based on the number of final links in the alternate route for a specific high-usage link \overline{h} ; for $\left| F_{\overline{h}} \right|$ and/or $d_{\overline{h}t} \left| F_{\overline{h}} \right|$ may be sufficiently large so that the total variable cost of installing circuits on the links of the alternate route for \overline{h} may exceed the total fixed and variable costs of installing circuits on that link. If such a high-usage links exists, an installation decision is made, and the circuit requirements of that link are updated as well as the sets H_{f} , $\overline{V}f$ such that $\overline{h} \in H_{f}$.

Steps 2 and 3 are for initial installations on final links. Since their circuit requirements cannot be routed, the links own requirements are to be met immediately. However, instead of merely satisfying the requirements of a final link \overline{f} , we try to exploit the economies of scale by installing large systems so that excess circuit capacity can be used for routing the circuit requirements of the high-usage links. After the installation decisions are completed, the circuit requirements are updated and excess circuit capacities are calculated.

Step 4: Given the installations on the final links made in Steps 2 and 3, we now turn to high-usage links again. In each period, we first consider the highusage link h with the smallest demand. If the links of the alternate route of h have unused circuit capacity, sufficient number of circuits are installed on those links to meet the requirements of h. Otherwise, we move on to the high-usage link with the next smallest circuit requirements and continue in this fashion until all high-usage links are considered in each period. The high-usage link circuit requirements are updated accordingly.

Obviously, it is possible that $d_{ht} \neq 0$ for some $h \in H$ at this point, and the question becomes whether routing is best for such a high-usage link or not. In <u>Step 5</u>, the high-usage links are ordered in ascending order of circuit requirements in each period. Starting with t = 1, the possibility of installing a system on h versus installing an additional system

on a link of the alternate route is explored. First, the two final links on the alternate route with the least excess capacity are identified. Of these two finals one (or both) having smallest excess capacity is now "eligible" to have a system 1 unit installed during t. Without the system added, the limiting factor for the alternate route is the excess capacity of this final. With the system added, the limiting factor is the minimum of the excess capacity of this final plus the capacity of a system 1 unit and the excess capocity of the other of the two. Second, the cheapest way of meeting the circuit requirements for this high-usage is found by considering only the alternatives described above. This least-cost solution will include a system and circuit configuration. However, only the system part of the solution is used here. In this step, as many circuits as possible are installed on the high-usage link; the remainder of the requirements being satisfied via the alternate route. (This is temporary, and Step 6 will adjust this solution.) The idea is to keep, for the moment, as much excess capacity on the finals so that the remaining high-usages to be considered will not be restricted more than necessary,

<u>Step 6</u> accepts the system configuration from <u>Step 5</u>, and decides whether it would be cheaper to "trade" some of the circuits on a direct high-usage link for circuits on the alternate route. If it is cheaper, then as many circuits as possible are rerouted along the final.

At the beginning of <u>Step 7</u>» the number of circuits to be installed on each link has already been determined[^]and thus we now re-evaluate the systems to be installed. We do this by solving a small 0*1 program which identifies the systems needed given the number of circuits to be installed on each link. This is where the discount rate plays a major role. For instance, it is possible that

$2w$
t s* $^{+w}$ t+i %* < v **i**++i^{+w} t+K; *-1.

that is, it may be cheaper to install two units of system s^* in period t and one unit of system s^* in period (t + 1) than one unit of system $(s^* + 1)$ in period t and one system of $(s^* - 1)$ in period (t + 1).

Numerical examples using HTCPQ are presented in the next section.

5. NUMERICAL EXPERIMENTS ON FOUR NETWORKS

In this section we consider four networks N_4 , N_2 , N_3 and N^{*} shown in <u>Figures 2</u>, f, f and f, respectively. Network N_4 is the one studied in [3] and [11]. The other three networks have been constructed arbitrarily. In each network there exists exactly one alternate route for each high-usage link. Various statistics for these four networks are given in <u>Table 1</u>.

For illustrative purposes, we use a 10-year planning horizon. Period 1, i.e. t » 1, is the base year of this planning horizon, t = 2 corresponds to the fifth year and t « 3 corresponds to the 10th year. The annual interest rate is set at 10%. The linkwise circuit requirements for $N_{\overline{1}}$, $N_{\overline{2}}$, N_3 and $N_{\underline{4}}$ are given in <u>Tables j2</u>, 3^{\wedge} ft^{and} 1 respectively. The circuit requirements in $N_{\overline{1}}$ are obtained from [3] whereas the circuit requirements in N^{\wedge} , $N_{\overline{3}}$ and $N_{\underline{4}}$ have been generated randomly except that all circuit requirements in period 1 were restricted to values not exceeding 1000. Furthermore, high-usage link requirements were in general chosen to be smaller than final link requirements. The rationale for the latter is that a typical final link carries the traffic of pointpairs other than its end-points.

Again, for illustrative purposes, we consider three alternate transmission systems. The fixed and variable costs of each transmission system, as well as the circuit capacity of each such system are based on a hypothetical rescaling of the data in <u>Table 1</u> of Yaged [18] and are given in <u>Table 6</u>. We should re-state here that facility costs are assumed to be independent of the actual length of the links. In <u>Table 7</u> «

we present the size of each Program Q corresponding to the four networks considered. The optimal solution for $N_{\overline{1}}$ is given in <u>Table 8</u>, This solution was obtained by using LINDO (14] on DEC-20 at Carnegie-MelIon University. The solution was found in 37 CPU minutes. We should emphasize that LINDO is a general branch-and-bound procedure and that the magnitude of the CPU time for our problem is not a reflection of the capabilities of LINDO. It is well-known that general procedures for solving integer programs of more than 100 variables do not perform well. (See, for instance, [4]*)

The 'approximate optimal¹ solutions for $N_{1'}$, $N_{2'}$, N_{3} and N_{4} in <u>Tables</u> f, <u>K</u>), ^L and <u>12</u>, respectively, have been obtained by the heuristic procedure HTCPQ. The entries under 'installations¹ in all those tables are the types of transmission systems to be installed on the respective link in the respective period. For instance, in <u>Table 8</u>, we see that one system 2 unit is to be installed on link 3 in period 2, and, in <u>Table 10</u>, we see that two different systems, namely 1 and 3, are to be installed on link 13 in period 1. A zero entry in any period for any link means that no system is to be installed. The number of circuits installed can be easily computed based on the systems installed.

A natural question that arises is on the 'goodness* of the HTCPQ solution. The best way to measure the heuristic solution is, of course, to compare it with the optimal solution. This we can do for N_1 : the optimal total cost is \$12,188,683 and the heuristic total cost is \$12,195,223, within 0.067* of the optimal. For networks N_2 , N_3 and N_4 , optimal solutions are not available. We have tried solving a reduced but equivalent <u>Program O</u> for N_2 using LINDQ, and we had to terminate the computations after 3 CPU hours.

We have also used measures of average circuit costs to test the goodness of the HTCPQ solution. All averages are stated in terms of present values.

- CL. [Average Circuit Cost Using Least Cost Circuits on High Usage Links]
 For each high-usage link the required number of circuits are met by System 3 circuits (the least cost ones), while no fixed costs are assigned to it.
- CQ [Optimal Solution Average Circuit Cost]
- C [Heuristic Solution Average Circuit Cost]
- C^ [Average Circuit Cost for No Network Hierarchy]

Observe that for both $\overline{C}_{\Lambda}^{\wedge}$ and $\overline{C}_{NS}^{\circ}$ there is no routing of circuits and the facility planning problem is solved for each final link individually. For the no network hierarchy case, of course, all links are treated as final links. The relaxations defining (L, R) result in a non-feasible solution to the 'original transmission planning problem and thus \overline{C}_{M} is an unattainable lower bound on \overline{C}_{n} Without the network hierarchy on the other hand, one obtains feasibility and consequently an upper bound on \overline{C}_{n} -

The average circuit costs for each of the four examples are given in <u>Table 13</u>.

Since HTCPQ itself is a heuristic procedure, there are likely to be other rules which improve the solutions possibly leading to an optimal solution. HTCPQ is consistent with the underlying rationale for constructing heuristic procedures, namely, to be able to find a good solution in realistic CPU times. However, for a specific problem a solution found by this procedure can possibly be improved by studying that specific problem. Finally, we report on numerical experiments using the heuristic under <u>partial restrictions</u> of the network hierarchy defined as follows. Each high-usage link whose alternate path length exceeds a pre-specified number m, is treated as a final link so that no routing of its circuits is permitted* Each of the remaining high-usage links in a partial restriction therefore has a timy-furm alternate path length of m.

In Figures 10 and 11 we plot the maximum length of any alternate path (horizontal axis) against the heuristic solution average cost (vertical axis) for networks $N_{\tilde{j}}$ and N_4 respectively. Observe that the minimum heuristic average cost is obtained at m = 6 for $N_{\tilde{j}}$ (Figure 10) and at m = 5 for N_4 (Figure 11). These findings support our conjecture that there is a maximum bound on the alternate path length above which it is not economical to route its circuits.

The CPU times for the four sample problems are in <u>Table 14</u>. HTCPQ was run on DEC-20 at Carnegie-Melion University. Computational complexity of the procedure is $0(n^2)$. The program is written in FORTRAN and has 729 lines.

6. CONCLUSIONS

The process of telecommunications network analysis includes at least three major tasks: (I) Trunking Analysis, (II) Switching Analysis, and (III) Transmission Facilities Planning. In this paper we have set forth a new mixed integer programming model for a class of problems of type (III) and have provided a heuristic procedure for solution. The basic simplifying feature of the model is that circuit requirements between any two nodes have only one way for which they may be alternatively routed. This restriction aids the development of closed form models, which are inter-temporally dynamic in that one period's decisions depend on decisions in the other periods in the fixed finite plan-The model highlights the interaction between large, fixed cost ning horizon. components and the much smaller marginal costs of additional circuit equipment. It facilitates the construction of a solution heuristic which incorporates fixed cost information, departing markedly from heuristics which depend heavily on marginal costs* No dynamic programing is required, and one can reasonably anticipate application to large scale problems. For those cases where it is important to consider a large number of alternate routes for point-pair circuit requirements, then our procedure could provide a good initial start for these more complicated computational methods, see Yaged [18].

Task (III) is certainly tied to Task (I) because circuit requirements for (III) are a result of having first solved problems of type (I), repeatedly, once for each time period within a fixed planning horizon. The output from (I) is actually more extensive, providing in addition, period by period circuit terminations and total switched traffic (in erlangs) at each node. These are some of the inputs required to solve problems of type (II). Using these inputs one could incorporate nodal cost models into the facilities planning models of this paper in order to account for switching costs and also in a related way to

account for nodal multiplexing costs.

The heuristic procedure has been illustrated on four numerical examples whose number of nodes and number of links are (8,15), (15,29), and (32,63) and (98,283), respectively. The sizes of the mixed integer prograoming problems of the latter two problems exceeds the capability of known mixed-integer programming algorithms and codes.

ACKNOWLEDGEMENT

We are grateful to Richard Edahl for the computational work and his suggestions to improve the heuristic algorithm.

APPENDIX: Procedure HTCPQ

For the heuristic method that will be given below, we need two new notations: let (i) yf_t denote the number of circuits on link f at the end of period t, and (ii) v_{ft} denote the unused transmission system capacity on final link f at the end of period t. That is,

$$y_{lt}^{i} = \sum_{s=1}^{s^{f}} \sum_{t=1}^{z} y_{lst}^{i}$$

and

$$v_{ft} = \sum_{s=1}^{s'} \sum_{t=1}^{t} a_{s} x_{fst} - y_{ft}^{t}$$

STEP 0: Find F, #h e H Set (i) $x_{lst} = y_{lst} = 0 \quad \forall \ l \in L, \ s \in S, \ t \in T$ (ii) $v_{ft} = 0 \neq f \in F, t \in T$ (iii) $y'_{lt} = 0 \quad \forall \ l \in L, \ t \in T$ If for any link 4, positive integer n $n a_{s'} \leq d_{l} < (n + 1) a_{s'}$, Set $x_{ls'1} = n$, $y_{ls'1} = na_{s'}$, $y_{lt}' = y_{ls'1}$ $d_{\ell t} = d_{\ell t} - y'_{\ell 1} \quad \forall t \in T$ <u>STEP 1</u>: ₩ h € H; (i) set $\overline{s} = 0$ (ii) if (a) $d_{h1} \ge a_2 + a_1$ or (b) $d_{h3} \ge 2a_2$ and $d_{h1} \ge a_2$ then set $\overline{s} = 3$ and go to (iii) if for $s^* \ni a_{s^*} \leq d_{h1} < a_{(s^* + 1)}$, $c_{s'1}'' (|F_{h}| - 1) d_{h1} \ge c_{s^{\star}}$ then set $\overline{s} = s^{*}$ (if $s^{*} > 0$) and go to (iii). Otherwise, go to (i) for next h. (iii) set $x_{h\overline{sl}} = x_{h\overline{sl}} + 1$, $y_{h\overline{sl}} = y_{h\overline{sl}} + a_{\overline{sl}}$ $y'_{ht} = y'_{ht} + \frac{a_s}{s}, d_{ht} = d_{ht} - \frac{a_s}{s} \quad \forall t \in T$ <u>STEP 2</u>: V f \in F; (i) if $d_{f3} < \bullet_s$, go to (ii) otherwise: set $x_{f31} = x_{f31} + 1$ $v_{ft} = v_{ft} + a_{s}$ t e T $y_{f3t} = y_{f3t} + b_t$ S t e T $y'_{ft} = y'_{ft} + b_t$ $v_{ft} = v_{ft} - b_t$

where
$$b_t = \min \{d_{ft}, a_{s}, \}$$

set $d_{ft} = d_{ft} - a_{s}$, $t \in T$
(i1) if $d_{f3} > a_2$ or $\sum_{t=1}^{3} (d_{ft} + \sum_{h \in H_f} d_{ht})/3 > a_2$, then set $\overline{s} = 3$.
otherwise, if $d_{f3} > a_1$, set $\overline{s} = 2$
otherwise, set $\overline{s} = 1$.
(i1) Perform operations A-2 and A-3 with $\overline{t} = 1$, $A = f$.
STEP 3: If, for any final link f, $d_{f3} > a_2$ and $x_{f31} \ge 2$, then set $\overline{s} = 3$.
Otherwise, compute $\overline{d}_f = \frac{1}{h}\Sigma d_{h2}$
Then, if $d_f - v_{f2} > a_1$, set $\overline{s} = 2$. If, on the other hand,
 $d_f - v_{f2} > a_2 + a_1$, set $\overline{s} = 3$. Then if $\overline{s} = 0$, go to the next link.
Otherwise, perform operation A-2 on f with \overline{s} and $\overline{t} = 2$.
STEP 4: When in increasing order of $d_{h\overline{t}}|F_h|$;
If $d_{h\overline{t}} < v_{f\overline{t}}$, $\overline{v}_{f\overline{t}}P_h$, perform operations A-3, with $L = s$, $\overline{v}_{f\overline{t}}P_{f}$ and
set $d_{h\overline{t}} = 0$, go to next link, otherwise define
(1) $v_1 = \min \{v_{f\overline{t}}\}$
(ii) $v_2 = \min \{v_{f\overline{t}}\}$ where \overline{t} is the link which gives v_1 in (1)
 $\frac{f \cdot \overline{t}}{f \cdot \overline{t}}P_h}$
(iii) $W = \Sigma M_f f_{\overline{t}}$ where $M_{f\overline{t}}$ is the marginal cost of
 $\frac{f \cdot \overline{t}}{f}P_{f}}$
adding one more circuit to link f in period t.
Then solve the following for \overline{t} :

STEP 3:

STEP 4:

<u>STEP 5</u>:

.

-

Program_Pe $\sum_{i=1}^{3} c_{s\bar{t}} x_{hs\bar{t}} + c_{1\bar{t}} x_{\bar{f}1\bar{t}}^{(1)} + \sum_{i=1}^{3} c_{s\bar{t}} y_{hs\bar{t}}^{(1)} + (MC+MC_{\bar{f}\bar{t}}) y_{\bar{f}s\bar{t}}^{(2)}$ min + $(MC+c_{1\bar{t}}^{*}) Y_{\bar{f}s\bar{t}}^{(3)}$ s. to: $\sum_{n=1}^{3} y_{hst}^{(1)} + y_{fst}^{(2)} + y_{fst}^{(3)} \ge d_{ht}$ ^yhsi $\frac{4}{2} a_{shst}^{2}$, s = 1,2,3 $y_{fst}^{i^2l} < v_{f1}$ yi³l < xi¹!
fst - *l*fIt $yl^{2}i + ygl < v.$ fst yfst - vf2 (1 ^yhst' ^yfst' ^yfst - 0 I 1 if a system 1 unit is added to \bar{f} in \bar{t} (a) x(1) =] Sir] 0 otherwise where fo) yJ.⁽¹⁾ is the number of system s circuits added to link h in period \overline{t} nst (c) yj is the number of system s circuits added to each fe F. <u>before</u> a new system s unit is added to \overline{f} . (d) $y_{=}^{(3)}$ is the number of system s circuits added to each ffF, after a new system s unit is added to f. Then, if (i) $x_{n,t} - \gg 1$, perform operation A-4, and (ii) $d_{ht} - > v_{.}$, perform operation A-2. Then perform operation A-3 VfeF_{in} with f = h.

<u>Step 6:</u> VhfH, in increasing order of $(y_{ht} - y_{n(t-1)})$;

If $y_{ht} - y_h (Z, T)^{m}$ or go to next link. Otherwise, compute MC = $Z \xrightarrow{MC} Z$ where MC- is computed as in <u>Step 5</u>, $f_f F_h$ Let S(L) be the last system installed on link h. Then solve the following:

$$\frac{\operatorname{Program} P_{f}}{\operatorname{min}} c_{\mathbf{s}(\mathbf{L})\mathbf{t}}^{*} \mathbf{y}_{hs\mathbf{t}}^{(1)} + \operatorname{MCy}_{fs\mathbf{t}}^{(3)}}$$
s. to: $\mathbf{y}_{hs\mathbf{t}}^{(1)} + \mathbf{y}_{fs\mathbf{t}}^{(3)} \ge \mathbf{y}_{h\mathbf{t}}$

$$\frac{\mathbf{y}_{hs\mathbf{t}}^{(1)} + \mathbf{y}_{fs\mathbf{t}}^{(3)} \ge \mathbf{y}_{h\mathbf{t}}}{\operatorname{Mcy}_{fs\mathbf{t}}^{-1}} \mathbf{y}_{h(\mathbf{t}-1)}$$

$$\frac{\mathbf{y}_{hs\mathbf{t}}^{(1)} - \mathbf{y}_{h(\mathbf{t}-1)}}{\operatorname{Mst}^{*} - \mathbf{y}_{h(\mathbf{t}-1)}}$$

$$\frac{\mathbf{y}_{fs\mathbf{t}}^{(1)} = \operatorname{Mcy}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{fs\mathbf{t}-1}^{(1)} \mathbf{y}_{hs\mathbf{t}}^{(1)} \ge \mathbf{0}$$
If $\mathbf{y}_{hs\mathbf{t}}^{(3)} \cdot \mathbf{0}$, go to next link. Otherwise;
set (i) $\mathbf{y}_{h\mathbf{t}}^{*} = \mathbf{y}_{h\mathbf{t}}^{*} - \mathbf{y}_{h\mathbf{t}}^{(3)} \mathbf{y}_{h\mathbf{t}}^{(1)} \mathbf$

Step 7; vfeL, solve the following:

Program P.,

Operations:

<u>Operation A-1</u>: S«t (i) $x_{hs1} - x^{*} \cdot 1$, (ii) $y_{h1}^{*} = y_{h1}^{*} + d_{h1}^{*}$ (iii) $Kl = y_{h2}^{*} + d_{h2}^{*}$, (iv) $y_{h3}^{*} = y_{h3}^{*} + d_{h3}^{*}$ and (v) $d^{*} - d_{h2} = d_{h3}^{*} = 0$.

<u>Operation A-2</u>: Set (i) x_r • x.r + 1 and (ii) v,. • v_ + a , • rts its it rt s t • t,...,3

<u>Operation A-3</u>: Set (i) $v^{+} - v_{ft} - d_{ft}$ and (ii) $y'_{ft} = y'_{ft} + d_{lt}$.

Operation A-4; Set (i) $x^{+} \cdot x^{+} + 1$ and $t^{*} \cdot t$.

(a) If t* > 3, return. Otherwise;

set yhf " ^{yh}t* ' V

Define $z^{*} = \min\{a_{n+1}, d_{n+2}\}$

Set d * =

Set t* • t* + 1

REFERENCES

- [1] Ash, G. R., R. H. Cardwell and R. P. Murray, "Design and Optimization of Networks with Dynamic Routing," <u>The Bell System Technical Journal</u> 60(1981) 1787-1820.
- [2] Baybars, I., K. 0* Kortanek, D. N. Lee and G. G. Polak, "Hierarchical Network Design for Facilities Planning: A 24-Node Example," <u>Proceedings</u> of the 13th Annual Conference on Modeling and Simulation, University of Pittsburgh, Pittsburgh, PA, April 22-£3, 1982.
- [3] Baybars, I., K. O. Kortanek and N. Mizuno, "A Mixed-Integer Programming Model for Transmission System Planning in Telecommunications Networks with General Circuit Requirements," <u>Proceedings of the 11th Annual</u> <u>Conference on Modeling and Simulation</u>, University of Pittsburgh, Pittsburgh, PA, May 1-2, 1980, pp. 395-402.
- [4] Christofides, N., <u>Graph Theory; An Algorithmic Approach</u>, Academic Press, New York, 1975.
- [5] Doulliez, P. J. and M. R. Rao, "Optimal Network Capacity Planning: A Shortest Path Scheme," <u>Operations Research</u>, 23(1975) 810-818.
- [6] Ford, L. R., and D. R. Fulkerson, <u>Flows in Networks</u>, Princeton Univ. Press, Princeton, New Jersey, 1962.
- [7] Freidenfelds, J., and C. D. McLaughlin, "A Heuristic Branch-and-Bound Algorithm for Telephone Feeder Capacity Expansion," <u>Operations Research</u>, 27(1979) 567-582.
- [8] Garfinkel, R., and G. Nemhauser, <u>Integer Programming</u>, Wiley, New York, NY, 1972.
- [9] Harary, F., Graph Theory, Addison-Wesley, Reading, MA., 1972.
- [10] Kochman, G. A., and C. J. McCallum, Jr., "Facility Location Models for Planning a Transatlantic Communications Network," <u>European Journal</u> <u>of Operational Research</u>, 6(1981) 205-211.
- [11] Kortanek, K. O., D. N. Lee and G. Polak, "A Linear Programming Model for Design of Communication Networks with Probabilistic Demand," <u>Naval Research Logistics Quarterly</u> 28(1981). 1-32.
- [12] Lawler, E. L., <u>Combinatorial Optimization, Networks and Matroids</u>, Holt, Rinehart and Whinston, New York, 1976.
- [13] Luss, H., "Operations Research and Capacity Expansion Problems: A Survey,¹¹ Operations Research 30(1982), 907-947.
- [14] Schrage, L., <u>User's Manual for LINDO</u>, The Scientific Press, Palo Alto, CA., 1981.

- [15] Shore, B., Operations Management, McGraw-Hill, New York, NY, 1973.
- [16] Smith, R. L., ^{lf}Deferral Strategies for a Dynamic Communications Network, "<u>Networks</u> 9(1979), 61-87.
- [171 Truitt, C. J., 'Traffic Engineering Techniques for Determining Trunk Requirements in Alternate Routing Trunk Networks,^{ff} <u>The Bell System</u> <u>Journal</u>, 33(1954), 277-302.
- [181 Yaged, B., "Minimum Cost Routing for Dynamic Network Models/¹ <u>Networks</u> 3(1973), 193-224.
- [191 Zadeh, N., "On Building Minimum Cost Communication Networks Over Time/¹ <u>Networks</u> 4(1974), 19-34.



Figure 1.* A Telecommunications Network

(I. Baybars and K. O. Kortanek)



Figure 2: Telecommunications Network N[^] (solid lines represent final links; dashed lines represent high-usage links)

(I. Baybars and K. O. Kortanek)



Figure 3: A Spanning Tree T of the Network ^ (Figure 2)

,

1

(I. Bayfaars and K. O. Kortanek)



÷.

Firgure 4: The Graph G_1 of N_1 consisting of spanning tree T (Figure 3) and the chord e_{15}

(I. Baybars and K. O. Kortanek)

.



Figure 5: A Telecommunications Network in which each high-usage link has two alternate routes

(I. Bayfaars and K. 0. Kortanek)



Figure 6: A Telecommunications Network in which each high-usage link has three alternate routes

(I. Baybars and K. O. Kortanek)

.



Figure 7: Network N₂

.



•. •

⁽I. Baybars and K. O. Kortanek)



.

Figure 9: Network N4

(I. Baybars and K.O. Kortanek)







		Final	High-Usage	Mia Alternate	Ave Alternate	Max Alternate
<u>Network</u>	Nodes	<u>Links</u>	<u> Links </u>	Path	Path	Path
'٦	8	7	8	2	3.25	4
 [№] 2	15	14	15	2	3.60	6
<u>-</u> N З	32	31	32	2	3.81	9"
- ™4	99	98	185	2 ·	6.30	30

. •

Tak	ole	1:	Nume	erical	Char	acte	rist	ics
of	the	Fou	r Ne	tworks	Nī,	^N ₂′	N ₃ ,	^N 4
		of	Fia	ures 2	2.7.8	.9.		

Link	<u>t-1</u>	<u>t-2</u>	<u>t-3</u>
1	35	60	70
2	21	42	63
3	92	184	184
4	58	99	174
5	47	80	188
6	47	80	177
7	59	100	177
8	2	5	8
9	17	34	68
10	17	39	51
11	7	14	21
12	18	31	72
13	18	31	72
14	18	36	54
15	18	41	72

Table 2: Circuit Requirements for N1

.

· Link	<u>t=1</u>	<u>t=2</u>	<u>t=3</u>
1	110	130	148
2	201	230	256
3	166	193	193
4	138	178	219
5	173	196	227
6	208	275	299
7	162	169	179
8	233	284	327
9	138	189	231
10	225	252	275
11	146	197	209
12	207	246	259
13	243	321	350
14	237	318	410
15	19	26	33
16	40	51	65
17	29	37	42
18	5	7	8
19	74	97	120
20	50	65 -	74
21	50	72	91
22	10	15	19
23	50	56	65
24	37	42	45
25	4	5	6
26	32	46	52
27	49	61	79
28	65	69	82
29	61	73	75

.

ł

Table 3: Circuit Requirements for N₂

-2

<u>Link</u>	<u>t-1</u>	$t \gg 2$	<u>t-3</u>
1	4	4	4
2	151	152	196
4	69	230 87	∠3∠ 111
5	248	409	609
67	32	47	67 267
8	181	250 320	367
9	271	382	546
10	41	43	45
12	168	255	331 334
13	116	142	147
14	128	238	242
15	68	313 92	403 110
17	2	2	2
18	276	425	539
20	72	85	85
21	111	157	191
22	51 60	54	70
24	157	262	343
25	144	192	255
26 27	24 71	45	* 47
28	114	105	119 217
29	24	31	34
30 31	130	201	293
32	24	26	333 30
33	25	42	55
34	30	49	68
36	21	22	20 29
37	16	23	30
38 39	25 15	25 21	34 27
40	7	13	16
41	17	29	31
42 43	9 18	16 24	22
44	11	15	15
45 46	25	34	38
40	13	18	27
48	21	23	31
49 50	52 9	78 15	81 15
51	19	19	25
52	56	81	98
53 54	15 42	15 53	20 73
55	66	72	90
56 57	17 11	17 16	18
58	15	15	18 18
59	14	15	21
60 61	22 15	30 25	44.
62	16	29	40
63	11	21	31

Table 4: Circuit Requirements for N3

Link	<u>t=1</u>	<u>t=2</u>	t=3	<u>Link</u>	<u>t=1</u>	<u>t=2</u>	<u>t=3</u>
1	489	537	563	45	112	183	195
2	167	260	343	46	404	472	641
3	- 125	245	257	47	238	276	369
4	276	380	497	48	240	472	646
5	162	241	281	49	314	329	454
6	422	481	596	50	297	412	412
7	315	365	390	51	459	610	671
8	332	590	590	52	218	398	577
9	288	313	444	53	478	497	591
10	181	260	293	54	408	518	574
11	256	471	546	55	347	440	484
12	234	425	616	56	430	756	816
13	262	393	471	57	363	468	496
14	378	495	712	58	237	367	502
15	351	491	500	59	484	566	616
16	283	390	522 [°]	60	498	6 9 2	851
17	497	656	728	61	397	432	622
18	160	212	226	62	410	`541	589
19	311	466	484	63	316	436	518
20	342	530	535	64	312	355	468
21	373	484	687	65	370	388	442
22	274	405	510	66	278	428	440
23	299	502	687	67	339	454	526
24	169	309	321	68	479	507	507
25	112	118	129	69	347	388	527
26	352	468	547	70	360	511	526
27	382	542	601	71	186	347	464
28	319	424	614	72	481	658	684
29	333	502	672	73	276	438	477
30	149	260	291	74	369	542	617
31	340	452	519	75	338	385	504
32	204	401	489	76	276	347	385
33	321	369	439	77	496	639	677
34	344	509	519	78	356	519	685
35	269	425	437	79	472	585	672
36	146	273	360	80	267	365	503
37	372	531	584	81	393	495	613
38	424	466	615	82	342	533	575
39	331	377	539	83	324	411	567
40	343	445	614	84	462	535	663
41	478	683	730	85	489	523	523
42	277	426	621	86	324	450	670
43	217	269	392	87	199	250	372
44	163	317	418	88	351	526	667

Table 5: Circuit Requirements for N_4

<u>Link</u>	<u>t=1</u>	<u>t»2</u>	t»3	<u>Link</u>	<u>t=1</u>	t»2	t»3
89	121	145	159	134	7		12
90	373	559	721	135	68	97	100
91	227	442	574	136	26	30	T03
92	324	612	862	137	14	30 1E	51
93	137	178	299	138	74	10	19
94	329	365	386	130	42	00 47	118
95	434	546	627	140	52 62	47	84
96	296	390	436	1/1	66	95	141
97	. 471	480	590	142	60	66	88
98	34	46	67	1/2	00	76	115
99	3	5	8	143	95 07	96	166
100	97	106	186	144	27	44	75
101	16	20	32	146	10	37	52
102	12	21	37	140	11	20	24
103	55	84	107	140	11	16	19
104	16	16	27	148	8	13	24
105	84	147	224	. 149	80	157 [.]	243
106	63	87	113	150	23	34	66
107	33	59	112	151	24	41	56
108	- 57	64	115	152	62	120	180
109	18	28	42	153	59	108	136
110	25	47	58	154	91	91	158
111	23	44	57	155	21	28	5_4
112	52	71	140	156	95	165	173
113	37	48	77	157	81	143	177
114	70	116	208	158	69	115	159
115	56	80	208	159	43	47	70
116	99	131	227	160	72	141	177
117	16	24	227	161	23	42	52
118	31	24 60	35	162	96	184	255
119	68	74	00	163	49	81	82
120	74	94	159	164	8	9	17
121	27	35	152	165	.79	146	233
122	29	55	00 77	166	41	75	130
123	16	20.	10	167	31	54	95
122	11	20 11	40	168	62	78	131
125	32	20	20	169	74	79	121
125	20	50	39	170	8	12	12
120	21	50	66	171	55	81	82
120	51	44	53	172	67	97	169
120	14	128	142	173	95	143	221
120	14 69	14 70	27	174	69	80	121
121	80	70	TT0	175	71	97	189
120	20 C 4	30	30	176	18	30	57
132 132	64 -	83 -	161	177	96	100	156
133	5	5	9	178	55	79	101

-- --

.

(Table 5 continued)

Link	<u>t-1</u>	<u>t-2</u>	<u>t»3</u>	Link	±^1	<u>t=2</u>	<u>t=3</u>
170	73	116	143	233	19	36	53
190	67	125	235	234	66	90	109
100 101	27	33	60	. 235	49	70	104
182	99	181	260	236	80	151	160
102 102	35	69	74	237	4	4	6
19/	94	145	159	238	41	75	75
104	53	102	189	239	52	60	81
105	79	102	112	240	79	109	119
100	24	105	128	241	87	125	150
100	27	63	86	242	69	91	157
100	52	154	177	243	46	46	84
100	92	101	212	244	43	64	118
190	51	291	212	245	24	46	46
102	04	25	26	246	62	65	89
192	19	20	20	247	85	105	123
193	35	51 10	10	248	56	59	97
194	14	10	80	249	77	112	132
195	37	22	33	250	17	32	52
196	22	22	55	251	63	75	90
197	4	60	71	252	12	22	33
198	32	02 10	71	253	86	149	213
199	13	102	2/ 104	254	7	8	8
200	65	103	107	255	38	69	* 80
201	86	131	107	256	40.	71	126
202	29	36	38	257	28	36	69
203	94	133	T08	258	94	145	184
204	34	37	53	250	17	21	22
205	77	132	201	259	18	27	30
206	67	132	137	200	-55	64	86
207	25	38	68	201	15	26	48
208	55	79	113	202	103	140	212
209	62	94	165	203	30	52	50
210	44	63	79	204	. 30	10	50
211	30	38	63	205	25	=9	01
212	32	59	68	200	57 69	32	128
213	76	123	227	207	00	·169	210
214	81	153	165	200	30	100	172
215	56	106	144	209	04	127	100
216	34	55	84	270	94 10	12	17
217	23	.44	51	271	16	27	51
218	78	135	202	272	25	27	20 DT
219	2	3	3	273	40	25 72	111
220	66	104	118	274	40	111	170
221	94	124	243	275	04 E1	20	140
222	60	90	142	270	51 70	90 117	220
223	11	12.	21	277	70	67	220
224	83	85	159	270	20	123	156
225	45	48	73	273	35 10	11	17
226	74	138	155	20∪ 201	85 T0	11 01	170
227	3	3	· 4	201 201	60 61	91 72	±70 Q1
228	99	132	211	202	57 52	73 50	110
229	69	75	93	203	54	53	112
230	12	17	17				
231	52	81	104				
232	82	106	T62				

.

.

.

.

SfMtm* (»)	ttaad Cost CcM (dollars) *	Variable Cost. (cª) (dollars) *	Capacity (a) (uni^s) *
1	530,000	3,100	30
2	•70,000	1,070	90
3	1,400,000	277	270

···· ··· ··· ··· ···

Table 6: Costs and Capacities of the Transmission Systems and Cost of the Circuits

Program 0							
<u>Network</u>	<u>Constraints</u>	Integer <u>Variables</u>	Other <u>Variables</u>				
^N i.	180	135	159				
- N 2	348	261	306				
N 3	756	567	663				
N	3396	2547	3102				

سرز

Table 7: Numerical Characteristics of the Associated Optimization Problems of the Four Networks

<u>Link</u>	Insi Si	tallati <u>t»2</u>	ions <u>t»3</u>
1	3	0	0
2	3	0	0
3	3	2	0
4	3	0	0
5	3	0	0
6	3	0	0
7	3	0	0
8	0	0	0
9	0	0	0
10	0	0	0
11	0	0	0
12	0	0	2
13	0	0	2
14	0	1	0
15	0	0	2

.

Table 8: Optimal Solution for N₁ (Total Cost: \$ 12188683)

	<u>Installations</u>					
Link	<u>t-1</u>	<u>t-2</u>	<u>t-3</u>			
1	3	0	0			
2	3	0	0			
3	3	· 2	0			
4	3	0	0			
5	3	0	0			
e	3	0	0			
7	3	0	0			
8.	0	0	0			
9	0	0	2			
10	0	0	0			
11	0	0	0			
12	0	0	0			
13	0	0	2			
14	0	0	2			
15	0	1	0			

Table 9: Heuristic Solution for N1 (Total Cost: \$ 12195223) 55

.(

Limk	<u>1nst</u> <u>t»1</u>	$\frac{t-2}{t-2}$	t»3
1	3	0•	0
2	3	2	0
3	3	1	1
4	3	0	0
5	3	2	0
б,	6	a	0
7	3	0	0
8	5	0	0
9	3	0	0
10	3	2	0
11	3	0	0
12	3	2	0
13 [']	4	.2	Z
14	3	3	0
15	0	0	0
16	2	0	0
17	1	1	0
18	0	0	0
19	2	0	0
20	0	0	0
21	2	0	0
22	0	0	0
23	· 0	0	0
24	0	0	0
25	0	0	0
26	0	0	C
27	2	0	C"
28	2	0	Ō
29	C	1	1

Table 10: Heuristic Solution for N₂ (Total Cost: \$ 34,411,299)

	' Inst	allat	1005	ı	Inst	allat	ions
Link	t=1	t=2	t=3	Link	<u>t=1</u>	<u>t=2</u>	<u>t=3</u>
- ·	شعية				•	_	-
1	1	C	0	33	0	1	0
2	3	0	C	34	2	0	0
3	3	D	0 -	35	0	0	0
4	3	0	C	• 36	0	0	D
5	• 4	3	3	37	0	0	0
E	3	C	С	38	0	0	2
7	3.	3	0	39	1	0	0
6	3	3	0	40,	D	0	0
9	6	0	1	41	1	D	1
10	2	0	0	42	0	D	0
11	5	0	0	43	0	D	D
12	3	2	0	44	D	0	0
13	3	0	0	45	0	D	1
14	3	2	0	46	1	C	C
15	3	3	0	47	0	Э	0
16	3	0	0	· 48	0	0	0
17	2	0	0	49	С	1	1
18	6	0	0	50	C	0	C
19	3	0	D	51	C	0	1
20	2	0	0	52	2	0	2
21	3	0	D	53	C	0	1
22	3	0	0	54	0	C	2
23	3	0	0	55	ε.	0	0
24	3	0	2	56	C	0	С
25	3	0	0	57	0	0	C
26	1	1	0	58	C	0	0
27	2	1	0	59	1	0	0
28	3	2	0	60	С	С	2
29	3	0	0	61	Ō	1	ē
30	3	3	0	52	ī	ā	1
. 31	3	2	0	E3		č	2
_32	٩	0	0		•	•	-

Table 11: Heuristic Solution for N₃ (Total Cost: \$ 63,143,533)

•

	Ins	tallaț	ions				
<u>Link</u>	<u>t»1</u>	t»2	<u>t-3</u>		<u>iş</u> s	talla	
-	-	•	~	Link		<u> </u>	≟=t
Ŧ	e	÷	C	41	. 9	3	0
Ž	3	3	ç	42	6	2	0
3	3	3		43	6	1	0
4	6	3	E ,	44	6	3	0
-	3	2	1	45	3	3	0
S	3	2	*	46	9	0-	0
т	6	0	2	47	3	Ţ	2
e		.⊑ 0	ž	48	6'	3	0
<u>.</u>	.s •	0	-	49	6	2	0
	e	0	<u> </u>	50	6	0	Ţ
n	6	3	0	51	6	3	0
12	6	0	2	52	6,	Ţ	3
13	6.	3	0	53	6	0	2
14	8	3	0	54	6	2	0
15	6	2	0	55	6	0	Ţ
16	6	2	0	56	9	3	0
. 17	6	3	0	57	6	3	0
18	· 3	2	C	58	6	3	0
19	6	0_ ہے	0	59	9	0	0
20	6	2	0	60	9	3	Ū
21	- 6	3	0	61	6	0	2
22	6	2	0	62	6	• 3	0
23	6	3	0	63	6	3	0
24	3	3	0	64	6	2	Ç
25	3	Q	0	65	6.	ō	0
26	5	<u>3</u>	Q	65	6'	3	0
27	6	2	0	67	6	3	Ō
28	6	0	2	66	6	Ţ	Ç
29	6	<u>1</u>	3	69	ē	0	0 ē
30	3	3	0	70	£	0	0
31	6	2	Q	71	3	3	0
32	<u>6</u>	<u>3</u>	Q	72	6	3	0
33	S	<u>3</u>	0	73	6	2	0
34	3	<u>3</u>	0	74	8	3	0
35	4	3	0	75	6	U	0
36	<u>3</u>	<u>3</u>	Q	76	6	0	0
37	S	3	0	77	9	0	0
38	G	•	2	78	6	3	0
39	E	б	0	79	9	U	0
40	<u>o</u> .	3	2	80	6	0	U

Table 12: Heuristic Solution for N₄ (Total Cost: \$ 473,501,920)

.

Installa		tions - · ·		Installations			
Link	t=1	t=2	t=3	Link	t=1	<u>t=2</u>	<u>t=3</u>
	-	<u>ن</u>					
81	6	2	0	121	2	0	0
82	6	3	0	122	Z	0	0
83	6	0	1	123	1	0	1
84	7	3	0	124	1	0	0
85	6	3	0	125	Z ·	. 0	C
86	6	0	3	126	2	0	C
87	6	0	0	127	C	C	C
88	6	0	3	128	C	i	C
89	3	0	0	129	C	G	C
90	6	1	3	. 130	0	C	2
91	5	3	2	131	1	0	0
92	5	4	3	132	2	0	2
9 3	6	0	0	133	1	0	0
94	6	. 3	0	134	0	0	0
95	9	0	0	135	0	0	0
96	6	0	٥	136	1	0	1
97 .	6	2	0	137	1	0	0~
98	3	2	0	138	0	0	2
99	0	0	1	139	2	0	. 0
100	3	0	0	140	3	0	0
101	0	0	. 0	141	0	0	2
102	0	0	0	142	0	0	2
103	0	1	2	143	2	0	0
104	0	0	0	144	0	0	0
105	0	0	3	145	1	1	0
106	1	0	1	146	0	0	1
107	2	0	1	147	0	0	0
108	2	0	1	148	0	0	0
109	0	· 0	0	149	0	0	3
110	0	0	1	150	0	0	1
111	0	0	2	151	1	1	0
112	2	0	2	152	3	0	0
113	0	0	2	153	0	0	0
114	1	0	2	154	0	ο	3
115	2	0	0	155	0	0	0
116	3	0	0	156	0	0	3
117	1	0	1	157	3	0	0
118	2	0	0	158	0	0	2
119	2	0	2	159	2	0	0
120	2	0	2	160	2	•	•

Installations					<u>installation</u>		tions	
I.Ink	<u>t-1</u>	.5=1	<u>t=3</u>		<u>İ•Ink</u>	<u>f1</u>	£= <u>2</u>	<u>f 3</u>
161	1	1	0		203	3	0	0
162 ÷	3	0	0		204	0	0	0
163 i	2	0	0		205	3	0	0
164 I	0	0	0		206	0	0	3
165	0	0	3		207	0	0	0
166	0	0	2		208	0	0	0
167 [`]	2	0	1		209	0.	1	3
168	2	0	2		210	0	0	0
169	2	0	2		211	0	0	0
170	0	0	0		212	0	0	1
171	0	0	0		213	3	0	0
172	3	0	0		214	0	0	2
173	3	0	0		215	0	0	1
174	2	0	2		216	0	0	0
175	3	0	0		217	1	1	0
176	0	•0	0		218	3	0	0
177	0	Õ	*		219	0	0	0
178	2	0	1		22C	0	0	2
179	3	0	0		221	3	0	0
180	0	0	3		222	2	0	2
181	0	0	0		223	1	0	0
182	3	0	0		224	2	0	2
163	2	v	0		225	0	0	2
184	0	0	2	1	226	0	2	P
ies	0	1	:3		227	0	0	0
186	2	1	Ċ		228	3	0	0
.187	2	0	2		229	2	0	T A
188	0	0	0	•-	230	L L	0	0
189	3	0	0		231	×৩ ০	0	Ť
19C	ţ	C	0		232	3 1	1	0
191	2	0	0		233	1 2	т Т	1
192	1	0	0		204	2	0	1
193	2	0	0		233	2	0	т Т
194	1	0	0		230	5	0	0
195	2	0	0		237	2	0	0
196	1	0	1	1	230	2	0	0
197	1	0	0		233	∠ ∩	0	1
198	2	0	0		240	0	0	2
199	0	0	С		241 242	2	0	∡ ∩
200	0	0	0		242 242	<u>د</u>	0	0
201	2	0	0		243	1	0	•?
202	1	1	0		411	-	0	-

(Table 12 continued)

<u>Link</u>	<u>Xnst</u> t=1	all.*± ±=2	ions t=3
245	0	0	0
246	0	0	0
247	2	0	C
248	2	C	C
249	C	0	С
250	C	0	С
251	0	0	0
252	0	0	0
253	3	0	0
254	1	0	0
255	2	0	0
256	2	0	2
257	0	0	2
258	3	0	0
259	1	0	0
260	0	0	0
261	0	0	0
262	1	0	Ţ
203	3	0	0
204	1	エ 2	0
205	2	2 0	1
267	0	0	т 3
268	3	Ő	2
269	0	0	3
270	3	0	0
271	1	0	0
272	1	0	1
273	0	0	0
274	0	1	2
275	3	0	0
276	0	0	0
277	0	0	2
278	0	0	0
279	3	0	0
280	0	0	0
281	3	0	0
282	0	0	2
283	2	0	1

.

.

(Table 12 continued)

.

	Ċ,	с о	°H	-
Network	Minimum	Optimal	<u>Heuristic</u>	Maxizsum
^N i	6228	8400	8405	10706
^N 2	5265	N.A.	7754	8217
N ₃	5933	N.A.	7791	8465
⁻ 4	4575	N.A.	6706	6906

• . •

_ Table 13: Average Circuit Costs: C_M(Average Using Least Cost Circuits on High _Usage Links); (L (Average Optimal); (^(Average Heuristic); C_{NS} (Average without Network Hierarchy

Network	CPU <u>Seconds</u>
۶	0.36
- ^N 2	0.41
^N 3	0.57
N4	3.98

Table 14: CPU Times